

PATENT NUMBER

## U.S. UTILITY Patent Application

<p>O.I.P.E.</p> <p>40 <u>GOB</u> <u>AA</u></p> <p>SCANNED <u>GOB</u> Q.A.</p>	<p>PATENT DATE</p>
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APPLICATION NO. 09/916618	CONT/PRIOR D	CLASS 365 <del>77X</del>	SUBCLASS <del>245</del>	ART UNIT 2818 <del>2824</del>	EXAMINER <del>6-5-2014</del> LWV
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## APPLICANTS

Geeng-Chuan Chern

**TITLE**

Self aligned method of forming a semiconductor memory array of floating gate memory cells with vertical control gate sidewalls and insulation spacers, and a memory array made thereby

PTO-2040  
12/99[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg. <b>8</b>	Figs. Drwg. <b>25</b>	Print Fig. <b>3L</b>	Total Claims <b>10</b>
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____  <b>SUPPLEMENTARY PRIMARY EXAMINER</b> <b>TECHNOLOGY CENTER 2800</b>	<i>Howard Weiss</i> <b>Patent Examiner</b> _____ (Assistant Examiner)		<b>NOTICE OF ALLOWANCE MAILED</b>  _____ (Date)	
	<i>David L. Jones</i> _____ (Primary Examiner)		<b>ISSUE FEE</b> Amount Due. <b>\$1600</b> Date Paid <b>8-8-03</b>	
	<b>ISSUE BATCH NUMBER</b> _____			
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	<i>K. Jones</i> _____ (Legal Instruments Examiner)		<b>ISSUE BATCH NUMBER</b> _____	

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(FACE)